1. A display comprising:

a semiconductor substrate;

a liquid crystal over semiconductor pixel array formed in said substrate; and

a memory coupled to said array, said memory also formed in said substrate.

2. The display of claim 1 wherein said pixel array includes a plurality of pixels each including a memory cell.

3. The display of claim 2 wherein said memory cells are static random access memory cells.

4. The display of claim 1 wherein said pixel array is coupled to said memory by a digital to analog converter.

5. The display of claim 1 wherein said memory includes a cell associated with each of a plurality of pixels of the pixel array.

6. The display of claim 1 wherein said pixel array forms a reflective liquid crystal spatial light modulator.

7. The display of claim 1 wherein said memory a dynamic random access memory, and said display includes a

refresh circuit, said refresh circuit adapted to refresh both said dynamic random access memory and said pixel array.

8. The display of claim 1 wherein said pixel array is adapted to eliminate the need for a periodic pixel refresh cycle.

9. A method for displaying information comprising:

forming a pixel array in a liquid crystal over semiconductor substrate; and

forming a memory in said liquid crystal over semiconductor substrate, with said memory coupled to said pixel array.

10. The method of claim 9 wherein forming a memory includes forming a memory associated with each pixel of said pixel array.

11. The method of claim 9 wherein forming a memory includes forming a volatile memory and refreshing said volatile memory and said pixel array in the same refresh cycle.

12. The method of claim 9 including displaying information without using a periodic refresh cycle.

13. A display comprising:

a memory array;

a pixel array; and

a refresh circuit coupled to said memory array and said pixel array, said refresh circuit adapted to refresh said memory array and said pixel array.

14. The display of claim 13 wherein said memory array and said pixel array are formed in the same semiconductor substrate with said refresh circuit.

15. The display of claim 14 wherein said substrate is a liquid crystal over semiconductor substrate, said pixel array including a plurality of electrodes adapted to interact with a liquid crystal material over said pixel array.

16. The display of claim 13 wherein said memory array is formed of dynamic random access memory.

17. A method for displaying information comprising:

providing a pixel array in a semiconductor substrate;

providing a memory array in said substrate; and

refreshing said memory array and said pixel array in the same refresh cycle.

18. The method of claim 17 including forming said memory and pixel arrays in a liquid crystal over semiconductor substrate.

19. The method of claim 17 including storing pixel data in said memory array.

20. The method of claim 17 including providing a liquid crystal material over said pixel array.

21. A processor-based system comprising:

a processor;

an interface bus coupled to said processor; and

a display coupled to said processor, said display including a liquid crystal over semiconductor substrate, said substrate including a memory array and a pixel array coupled to said memory array.

22. The system of claim 21 wherein said memory array includes a plurality of cells, each cell coupled to a pixel of said pixel array.

23. The system of claim 22 wherein said memory cells are static random access memory cells.

24. The system of claim 23 wherein said pixel array is a reflective liquid crystal array.

25. The system of claim 24, said memory including a plurality of storage locations at each pixel and a digital to analog converter coupling each of said storage locations to a different pixel cell.